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APPLICANT(S):
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SMD	A.	5,083,204	01/21/92	HEARD ET AL.			
	B.						
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
SMD	I.	EP 0 942 603	09/15/99	EPO				
SMD	J.	EP 0 667 582	08/16/95	EPO				
SMD	K.	EP 0 834 816	04/08/98	EPO				
	L.							
	M.							
	N.							
	O.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SMD	P.	KHAN M. S. ET AL: A STANDARDIZED INTERFACE CONTROL UNIT FOR HETEROGENOUS DIGITAL SIGNAL PROCESSORS, INTL. SYMPOSIUM ON CIRCUITS AND SYSTEMS (ISCAS), DIGITAL SIGNAL PROCESSING (DSP), LONDON, MAY 30-JUNE 2, 1994, NEW YORK, IEEE, US. VOL. 2, MAY 30, 1994, PGS. 97-100.
SMD	Q.	DUTTA S. ET AL., ARCHITECTURE AND DESIGN OF A TALISMAN-COMPATIBLE MULTIMEDIA PROCESSOR, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO TECHNOLOGY, IEEE INC. NEW YORK, US, VOL. 9, NO. 4, JUNE 1999, PGS. 565-578.
	R.	
	S.	
	T.	

EXAMINER *S. Deckter*

DATE CONSIDERED *6/26/02*

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